

Certificate of Publication



This is to confirm that

Shikhar Bajpai

Published following article

Design and Analysis of High Throughput and Energy Efficient FIR Filter using Retiming and Pipelining in Verilog Coding

Volume 10, Issue 10, pp: 138-145

www.ijres.org

A Peer Reviewed referred Journal

International Journal of Research in Engineering and Science (IJRES)

Editor-In-Chief

ISSN: 2320-9364 IJRES is Peer Reviewed Refereed.



Certificate of Publication



This is to confirm that

Krishna Raj

Published following article

Design and Analysis of High Throughput and Energy Efficient FIR Filter using Retiming and Pipelining in Verilog Coding

Volume 10, Issue 10, pp: 138-145

www.ijres.org

A Peer Reviewed referred Journal

International Journal of Research in Engineering and Science (IJRES)

Editor-In-Chief

ISSN: 2320-9364 IJRES is Peer Reviewed Refereed.